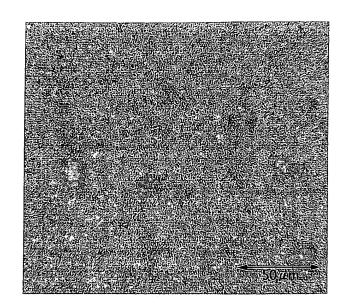


**F05.1A** 



**F**[[][]. 1B

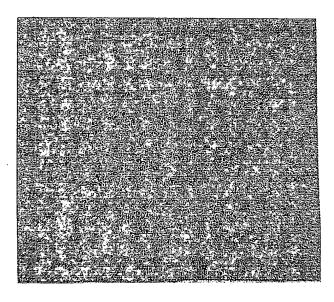
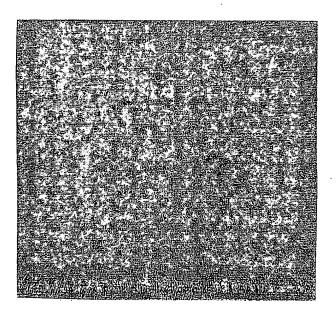


FIG. 1C



IF [[[]]. 2A

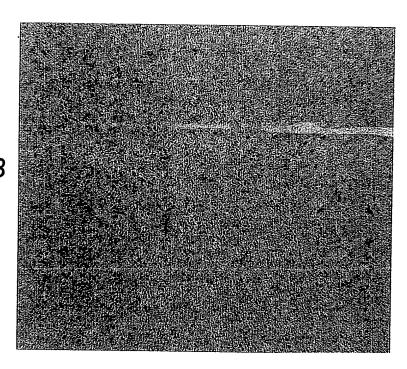
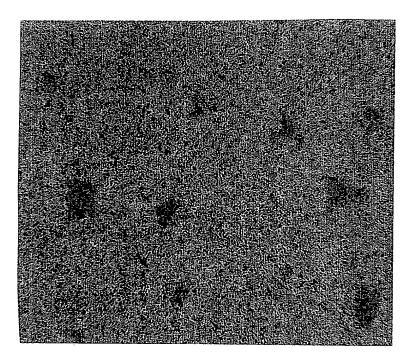


FIG. 2B



IF [[[□]. 3A

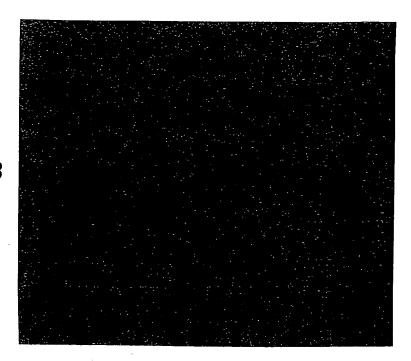
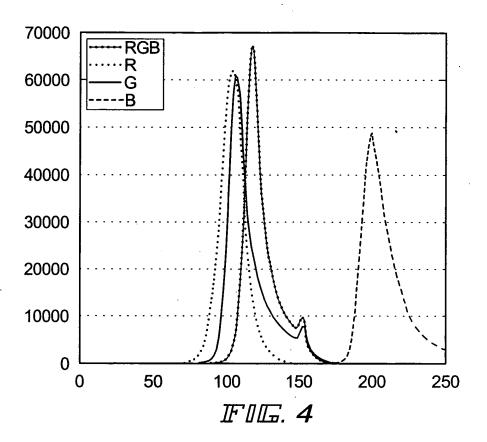
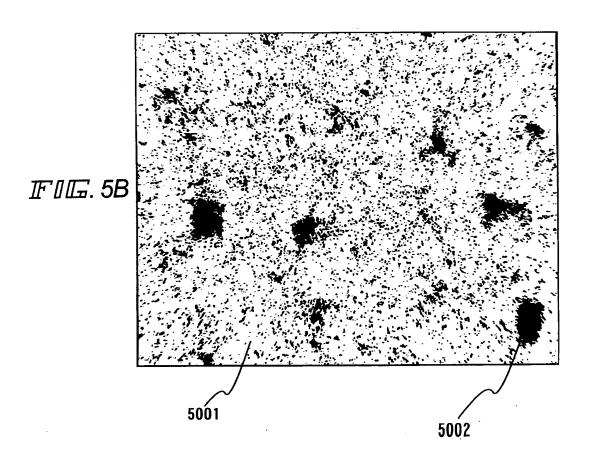
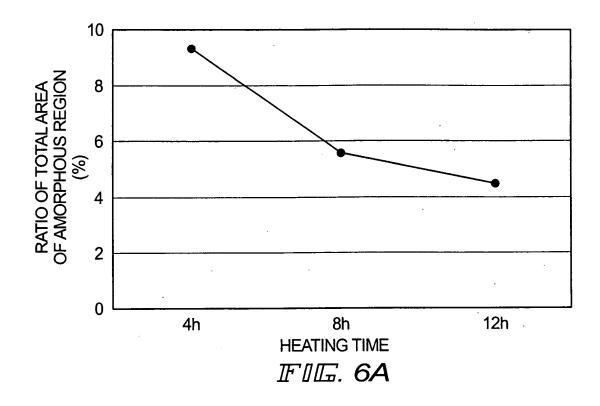


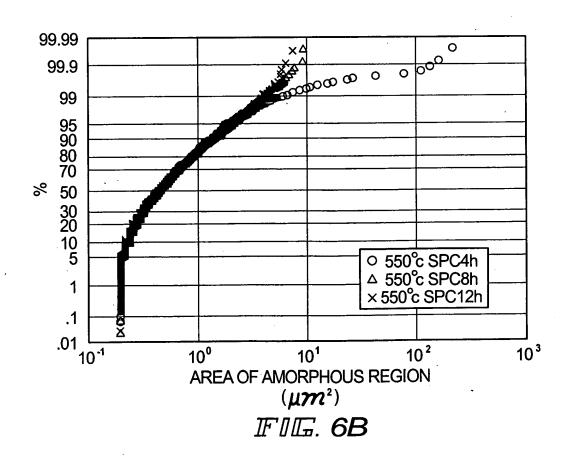
FIG. 3B



**』[[] ... 5A** 







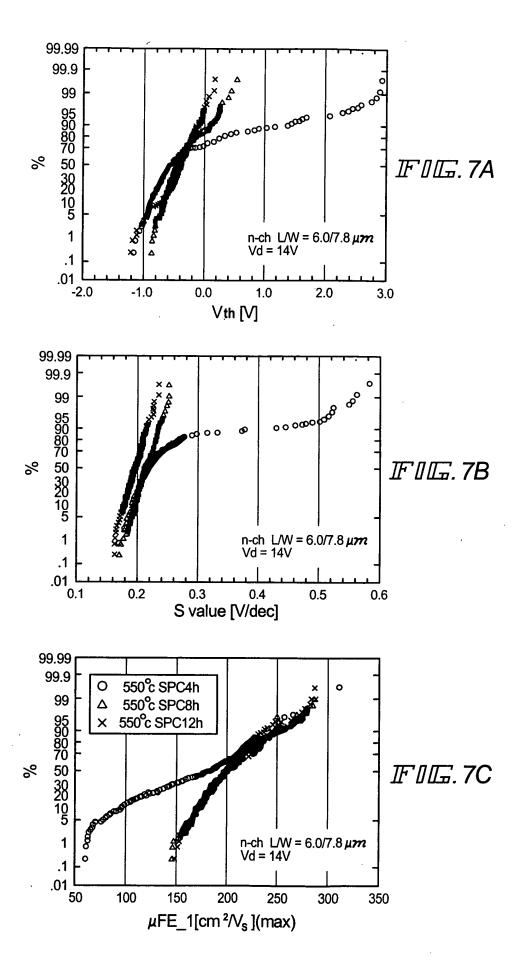
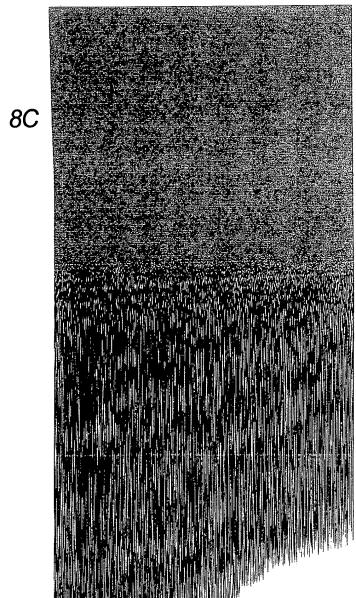
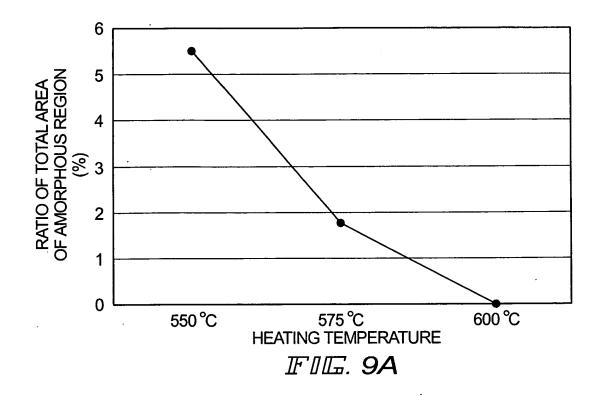
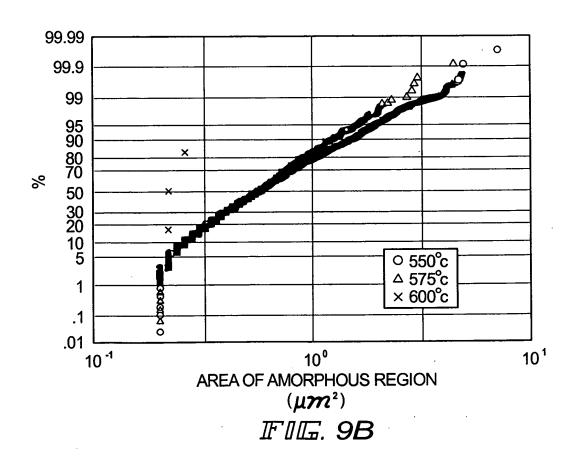


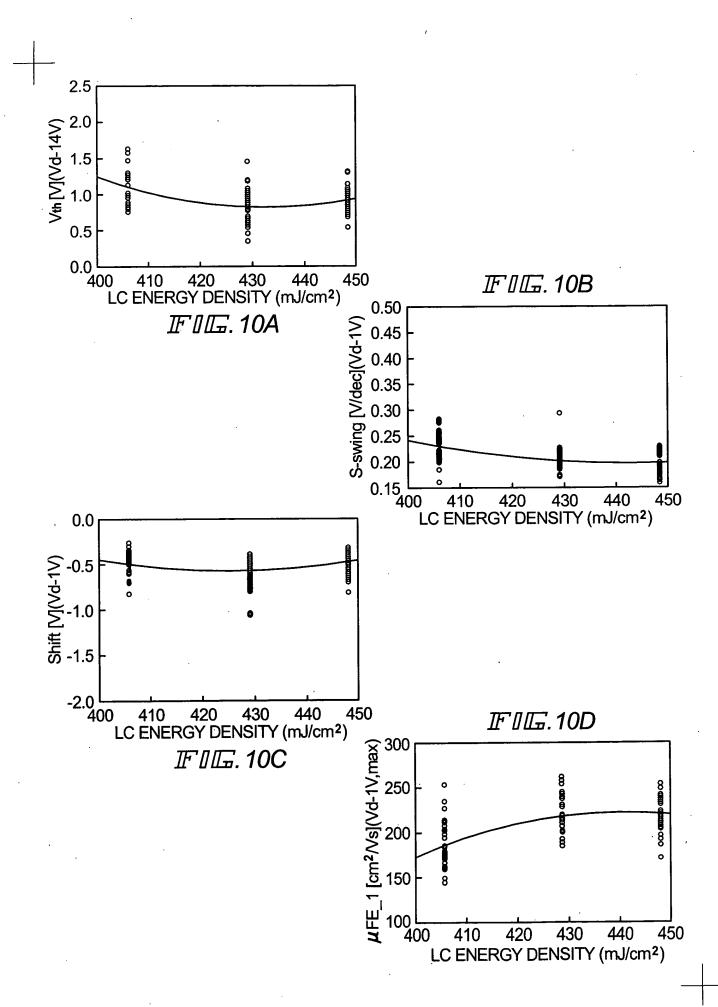
FIG. 8B

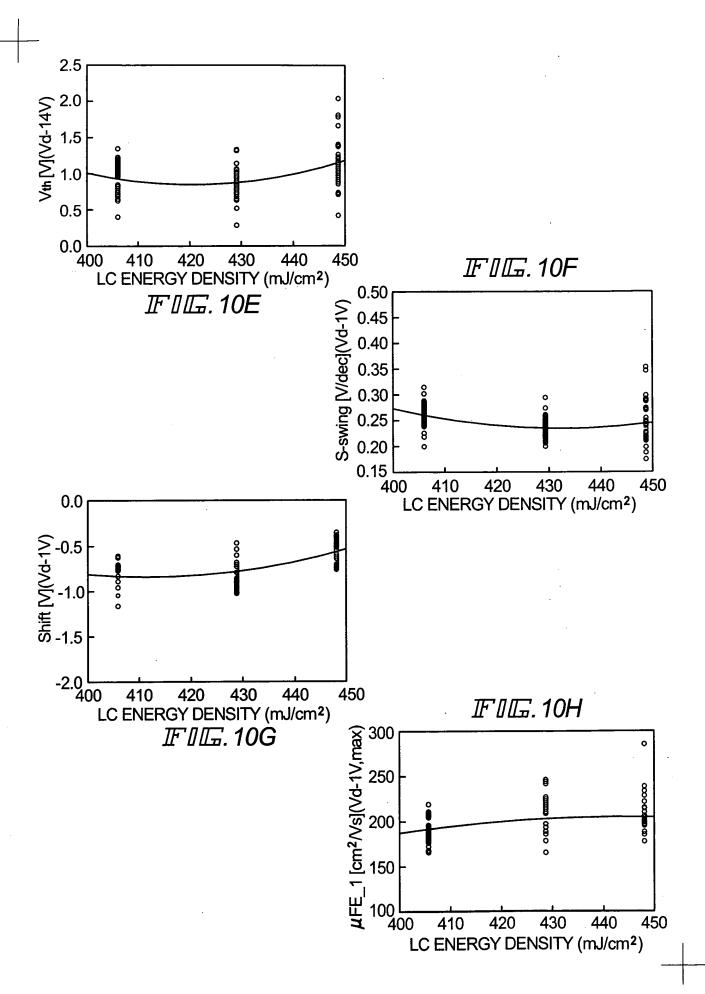


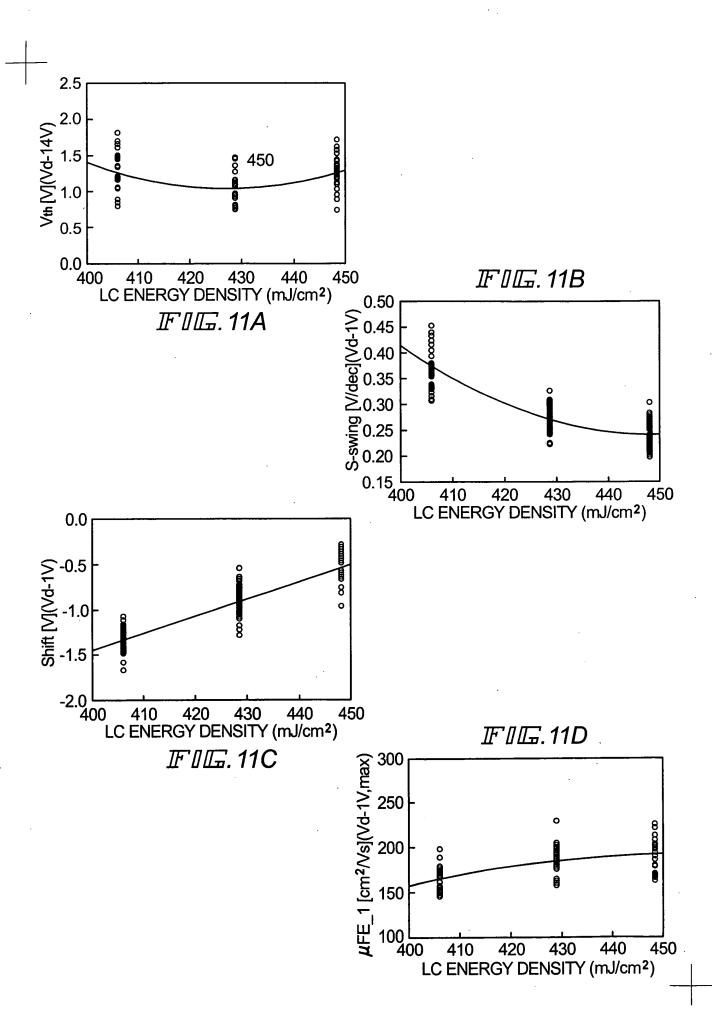
*IF∥IIIIE.* 8C











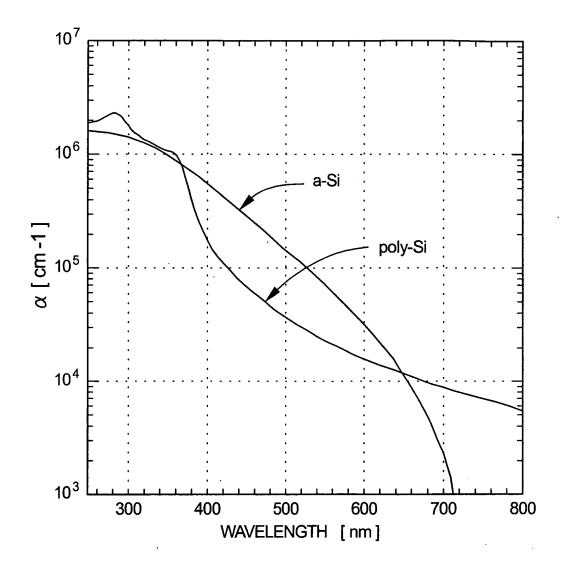
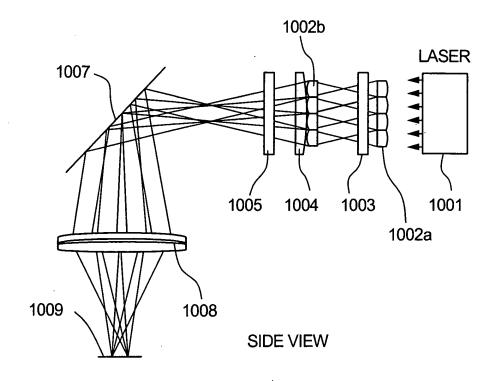
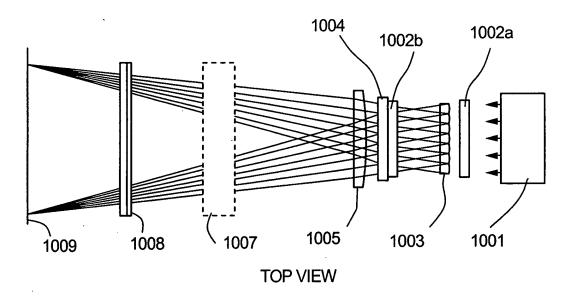
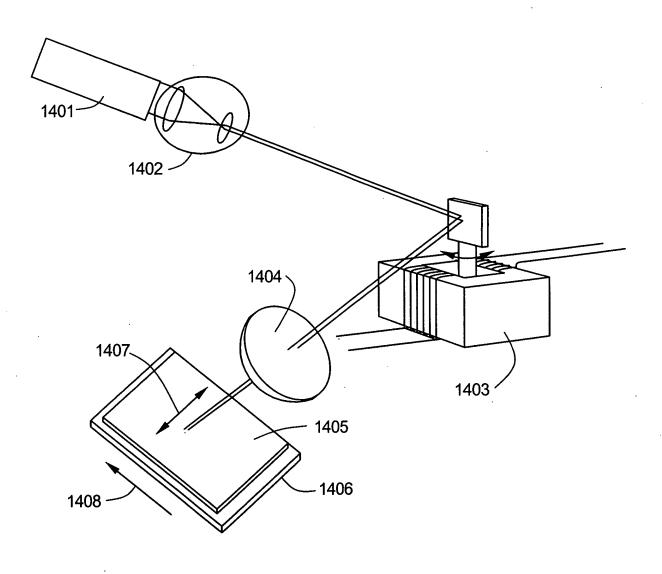


FIG. 12

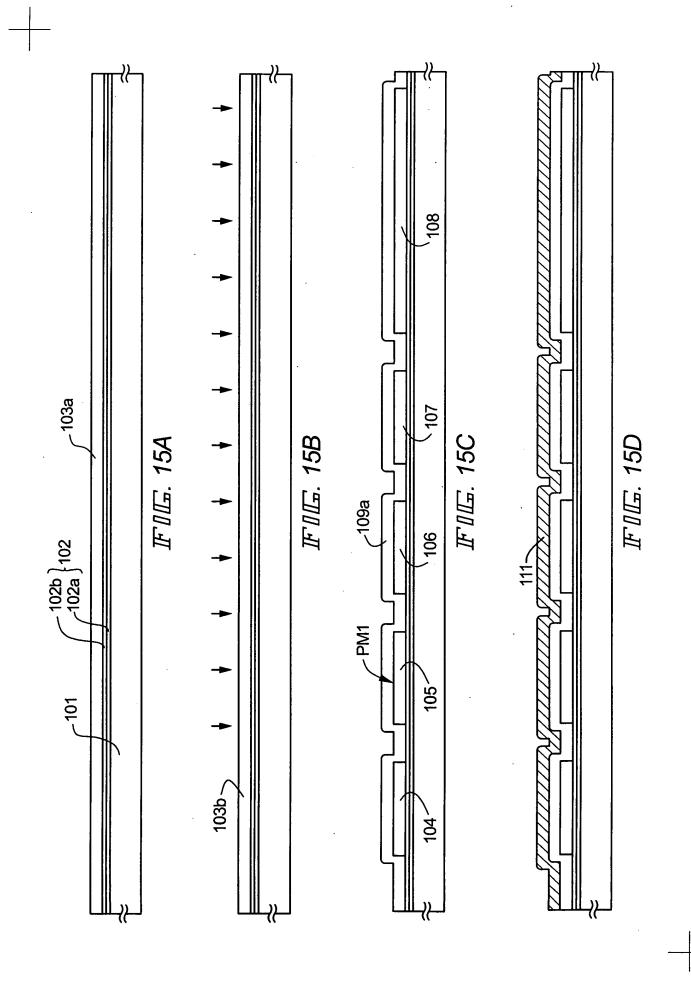


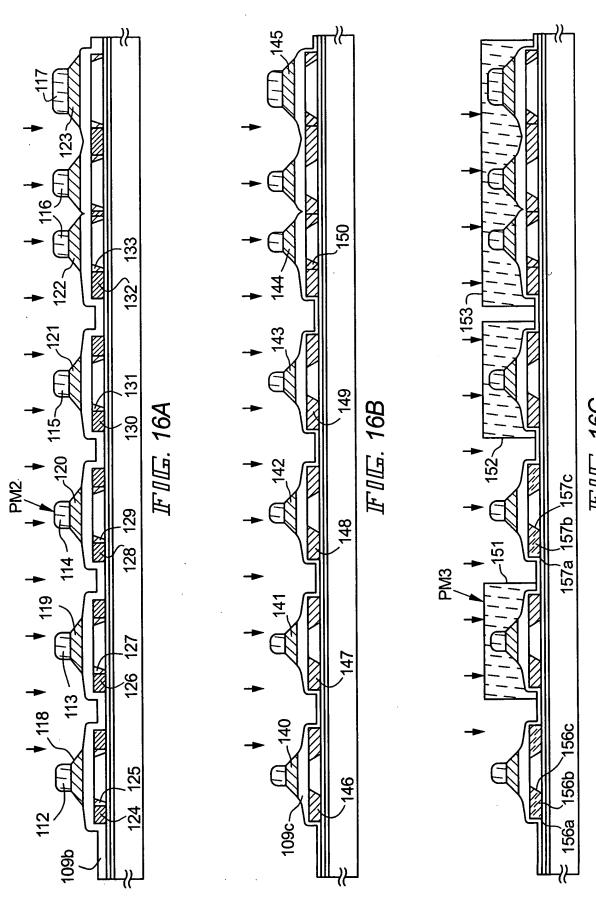


**FIG. 13** 

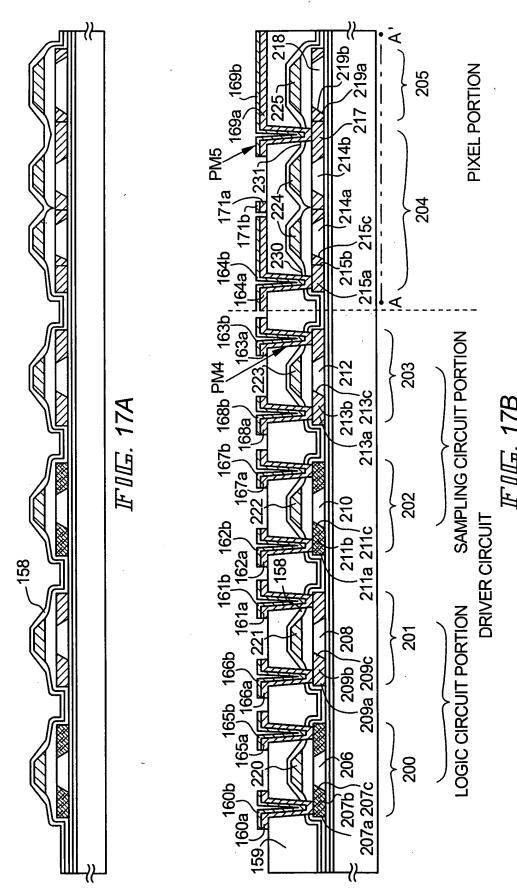


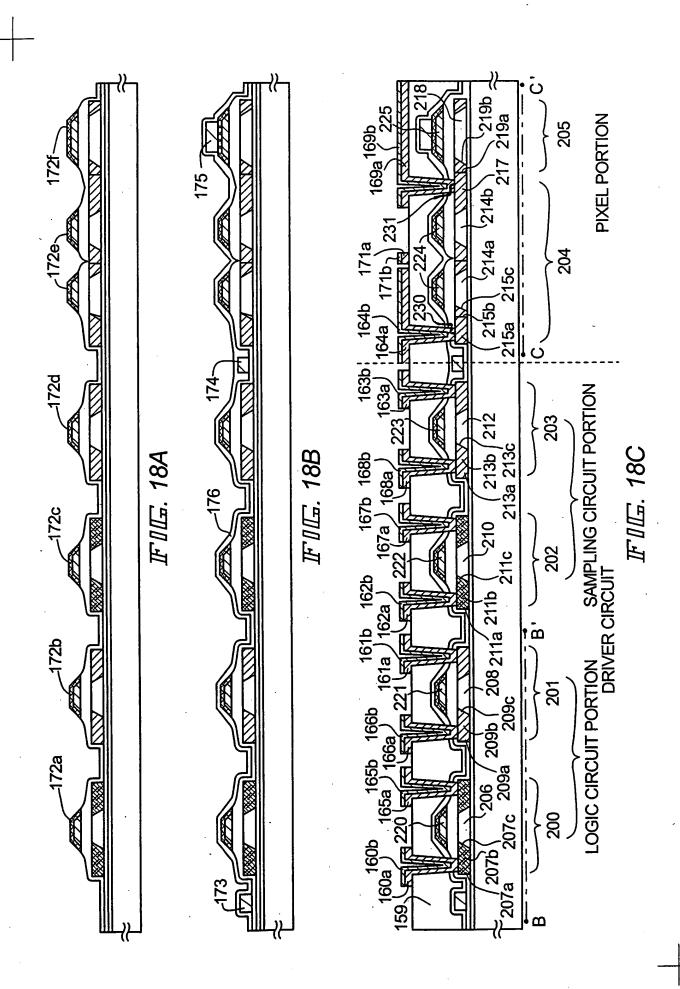
**肾圆层. 14** 

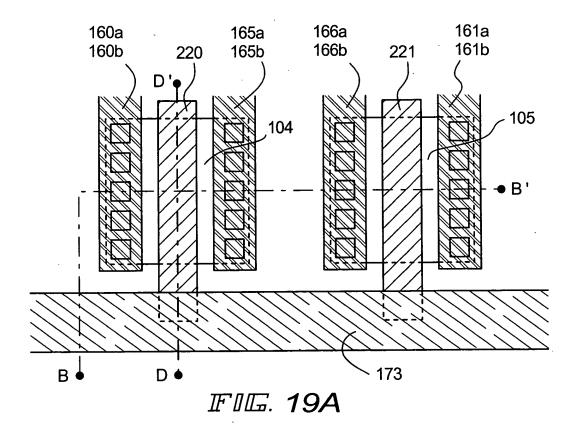


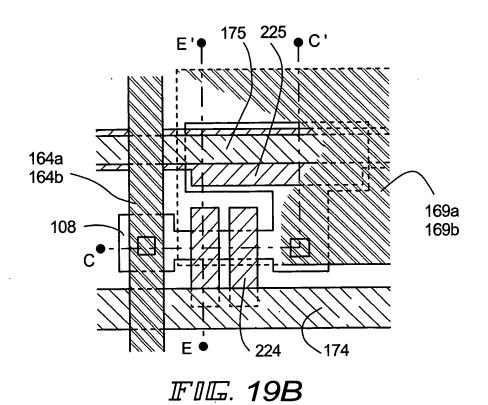


**正**[[压] 16C









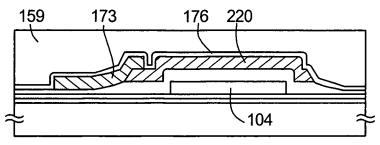


FIG. 20A

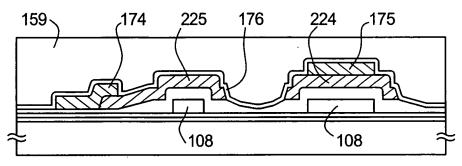
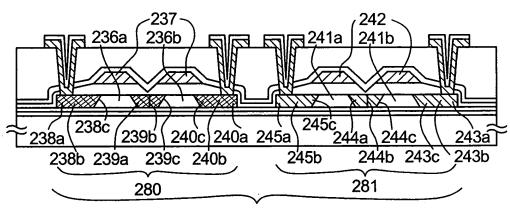
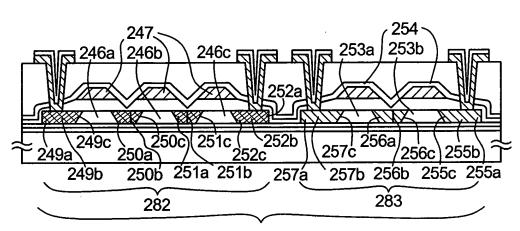


FIG. 20B



LOGIC CIRCUIT PORTION
FIG. 21A



SAMPLING CIRCUIT PORTION IF III. 21B

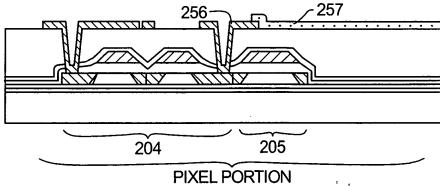
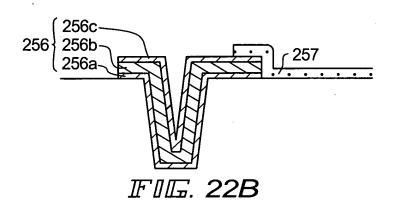
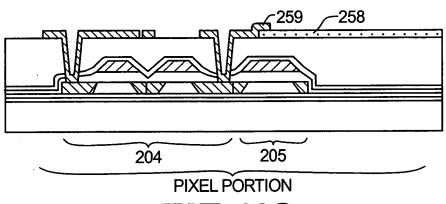


FIG. 22A





**FIG. 22C** 

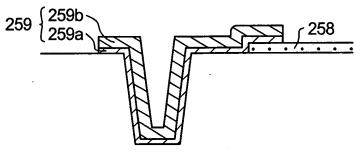


FIG. 22D

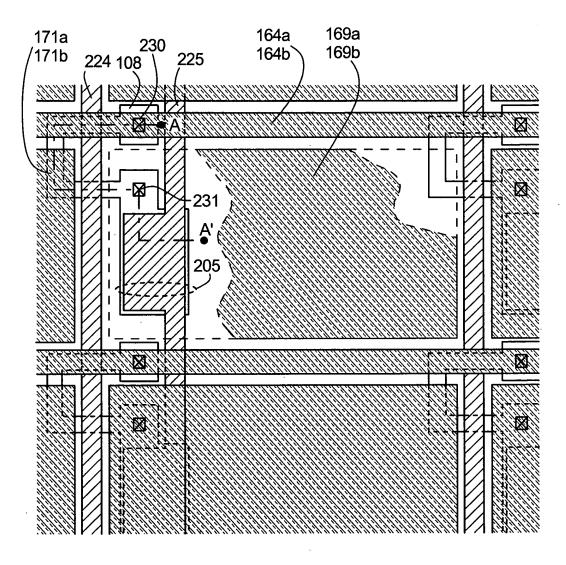
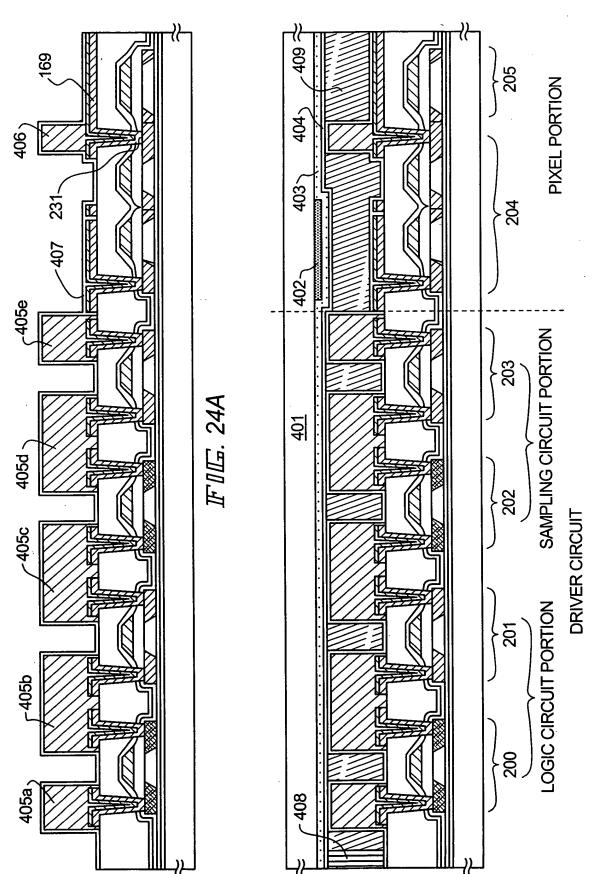
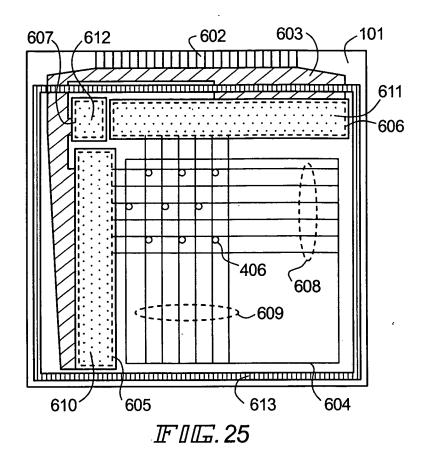
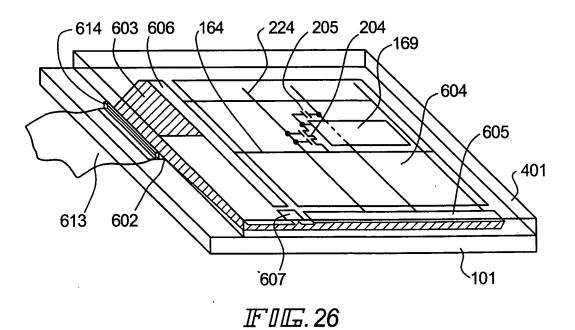


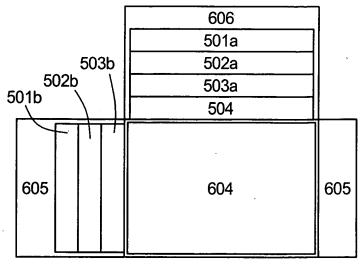
FIG. 23



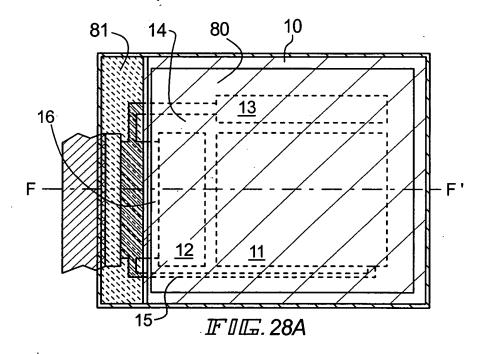
**正**015. 24B

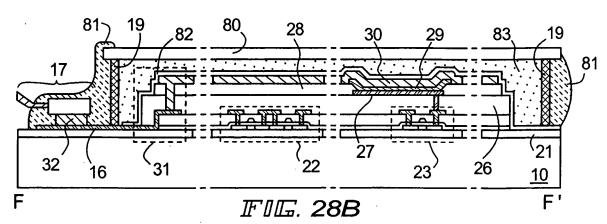


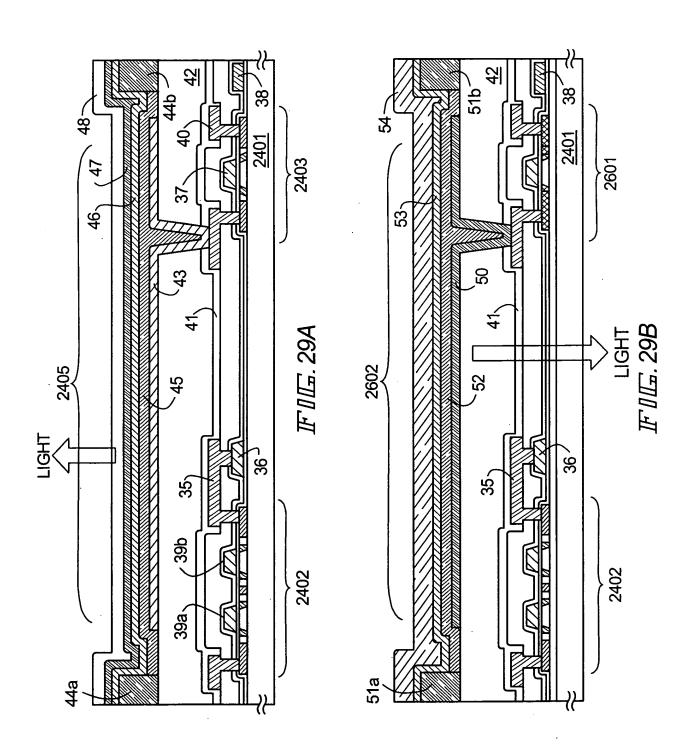


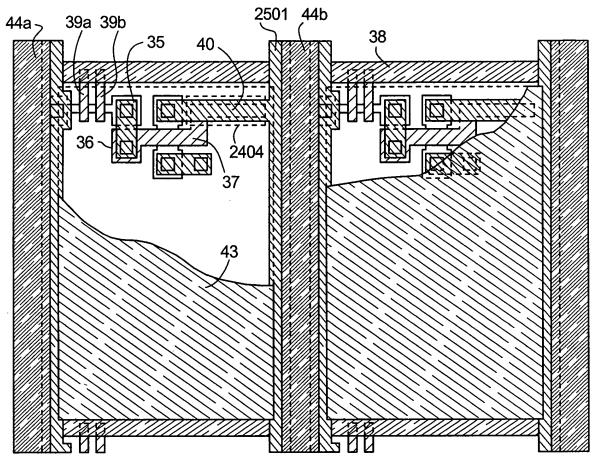


**FU**伝. 27

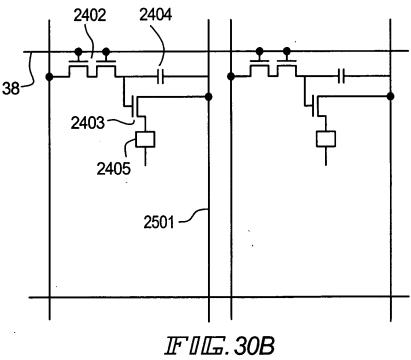


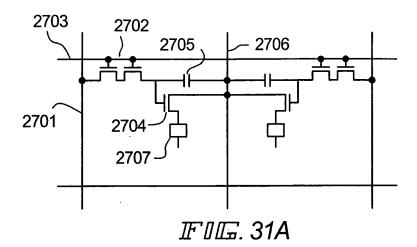


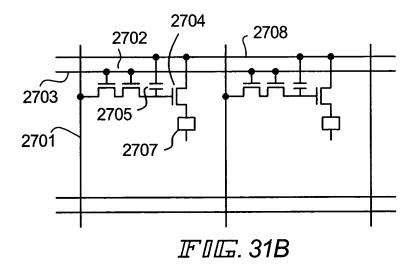


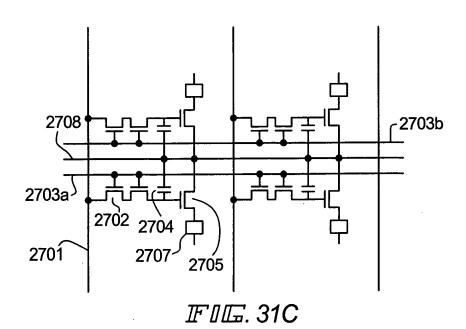


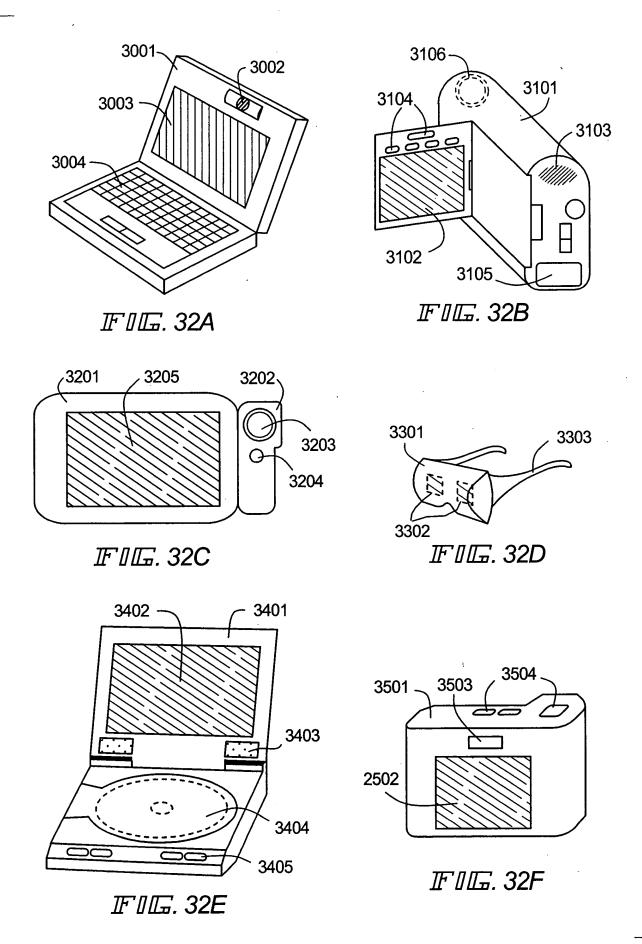
IF [[□]. 30A

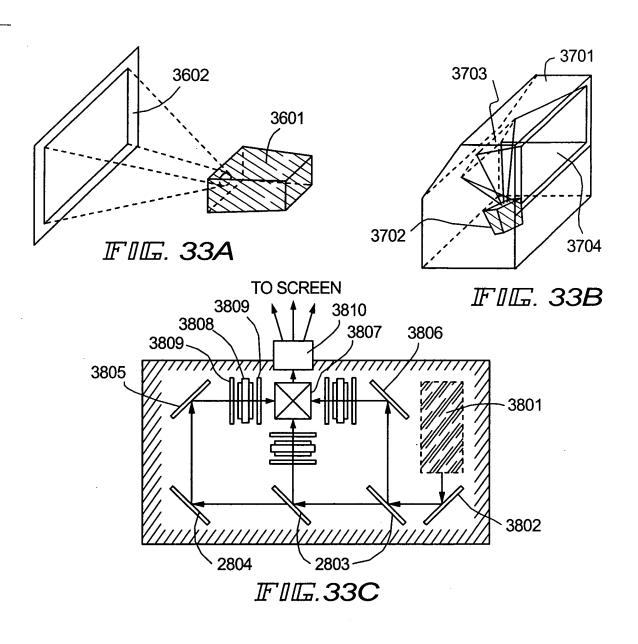


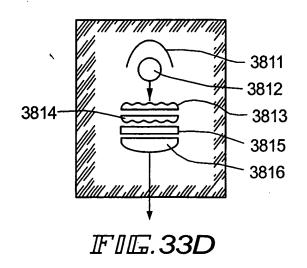


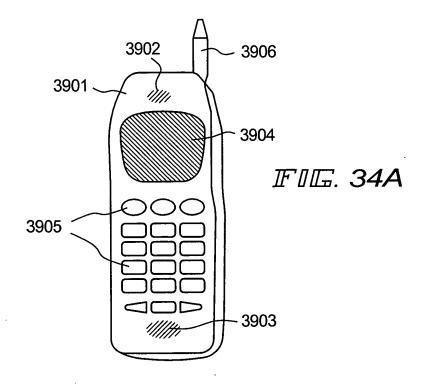


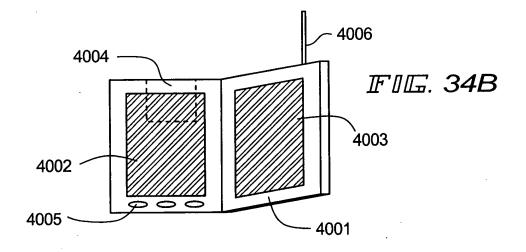


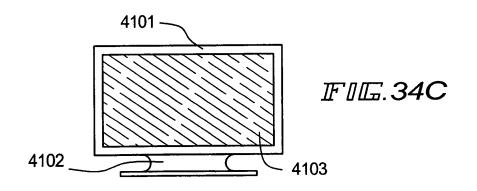






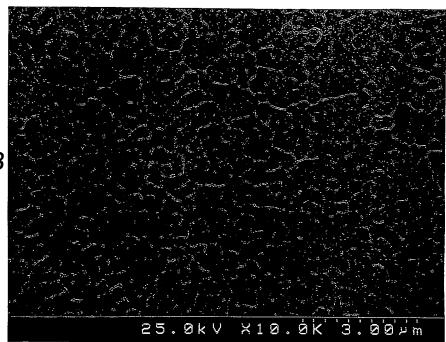






25.0kV X10.0K' '3.00µm

IFIII. 35A



**F**□**□**. 35**B** 

**₮₮₽**. 36A X10.0K 3.00µm 25.0kV

**FI**匠. 36B